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'analog', putting systems  
on a chip requires  
putting analog interfaces  
on the same chip as  
digital processing  
functions. Since some  
processing functions are  
accomplished more  
efficiently in analog  
circuitry, chips with a  
large amount of analog  
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ringing on the RLC tuned circuits that form the chip/package power supply rails and off-chip drivers and receivers, coupling between circuits through the chip substrate bulk, and radiated emissions from the chip/package interconnects. To aggravate the problems of designers who have to deal with the complexity

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of mixed-signal coupling there is a lack of verification techniques to simulate the problem.

In addition to

considering RLC

models for the various chip/package/board

level parasitics, mixed-signal circuit designers

must also model

coupling through the

common substrate when

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an accurate estimate of coupled noise in their designs. Unfortunately, accurate simulation of substrate coupling has only recently begun to receive attention, and techniques for the same are not widely known. Simulation Techniques and Solutions for Mixed-Signal Coupling in Integrated Circuits addresses two major

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issues of the mixed-signal coupling problem -- how to simulate it and how to overcome it. It identifies some of the problems that will be encountered, gives examples of actual hardware experiences, offers simulation techniques, and suggests possible solutions. Readers of this book should come away with

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a clear directive to simulate their design for interactions prior to building the design, versus a 'build it and see' mentality.

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